

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:)	
)	
Gill)	Group No.: (unknown)
)	
Serial No.: (unknown))	Examiner: (unknown)
)	
Filed: (herewith))	Docket No. HSJ920030029US1
)	
For: SPIN VALVE TRANSISTOR WITH)	
SELF-PINNED ANTIPARALLEL)	
PINNED LAYER STRUCTURE)	

Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

Sir:

INFORMATION DISCLOSURE STATEMENT

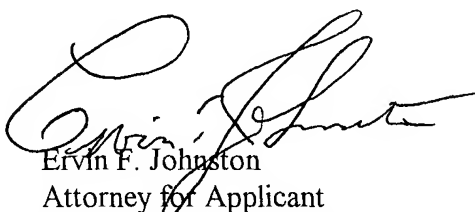
Applicant hereby cites the documents listed in accompanying Form PTO-1449 with respect to the above-referenced patent application under the provisions of 37 C.F.R., Sections 1.56, 1.97 and 1.98. Copies of the documents are attached.

The filing of this Information Disclosure Statement will not be construed to mean that a search was conducted or that no other material information, as defined by 37 C.F.R. 1.56, exists.

The Examiner is respectfully requested to make the listed documents of record in connection with the prosecution of the subject application.

Respectfully submitted,

Date: July 17, 2003

By: 
Ervin F. Johnston
Attorney for Applicant
Registration No. 20,190

Form PTO-1449

**INFORMATION DISCLOSURE CITATION
IN AN APPLICATION***(Use Several Sheets If Necessary)*Docket #
HSJ920030029US1

Application No. (unknown)

Applicant: Gill

Filing Date: (hour with)

Group Art Unit (unknown)

U.S. PATENT DOCUMENTS

XAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	6,480,365 B1	11/12/02	Gill et al	360	324.11	12/09/99
	6,501,143 B2	12/31/02	Sato et al	257	421	06/29/01

FOREIGN PATENT DOCUMENTS

	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
						YES	NO

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

EXAMINER**DATE CONSIDERED**

EXAMINER: Initial if citation is considered, whether or not citation is in conformance with MPEP § 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.

(2/92 PTO)